

Project on: "Development of an efficient staple yarn characterization unit with multi sensor fusion and field programmable gate array (FPGA) based data reduction card"

[Sponsoring Authority: DST, Govt. of INDIA]

**Department of Electrical Engineering
Bengal Engineering and Science University, Shibpur
Howrah-711 103**

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[University Project Code: DRC/DST-IDP/EE/AS/018/11-12]

Applications are invited for one post of Junior Research Fellow (JRF) to work in a DST-IDP Project in the Electrical Engineering Department in the area of Analytical/Industrial/Sensors & Allied Instrumentation.

No. of posts: 1(one)

Essential Qualification: First class M.E / M. Tech in Electronics and Telecommunication Engineering / Electrical Engineering / Instrumentation Technology from a recognized University.

Desirable Experience: Experience in the field of embedded system design is preferred.

Fellowship: Rs.18,000/- + HRA + MA per month (for first two years)
Rs.20,000/- + HRA + MA per month (for 3rd year).

Duration: Three years.

The posts are purely temporary and co-terminus with the tenure of the program. No TA/DA will be paid to the candidates if called for interview. The Fellow shall have an opportunity to specialize /enhance qualifications in this area of specialization.

Interested eligible candidates may send their applications on plain paper giving detailed bio-data and photograph, enclosing attested copies of testimonials to reach Dr. Anindita Sengupta, Principal Investigator, DST-IDP Project, Department of Electrical Engineering, Bengal Engineering and Science University, Shibpur, Howrah – 711 103 within 21st November, 2011.

Registrar