

Project on: "SMDP-II Project"

[Sponsoring Authority: DIT, Govt. of India]

**School of VLSI Technology
Bengal Engineering and Science University, Shibpur
Howrah-711 103**

[University Project Code: DIT/VLSI/HR/286]

Interested candidates are requested to appear for a walk in-interview on 10th August 2012 at 3.00 P.M., for the following temporary posts for SMDP-II, the School of VLSI Technology, Bengal Engineering and Science University, Shibpur, Howrah.

Project Faculty: 1, Lab Engineer:-1

Essential Qualification: First class M.E / M.Tech in Electronics and Communication Engineering / Micro-Electronics and VLSI Design from a recognized University.

Desirable Experience: Teaching and industrial experience in the field of VLSI is preferable. One year experience is essential.

Fellowship: Rs.24000/- per month, depending upon the qualification and experiences.

Duration: 7th months or until the end of the project.

Competent Candidate should bring two hard copies bio-data to submit and original mark sheet and certificate.

Venue of the interview:

Office of the Director (Research and Consultancy),
6th Floor, New Technology Building, BESU, Shibpur.

NB: Soft copy of the bio-data may be sent through e-mail in advance to:
Prof. Hafizur Rahaman,
E-mail id: rahaman_h@yahoo.co.in /rahaman_h@it.becs.ac.in

Registrar

(W. Code DRC- 016/12-13)

[Download the notice for recruitment of Project Faculty and Lab Engineer in VLSI \[PDF File\]](#)